


Application Number 	Application No. 10/678,139	Applicant(s) YAMAZAKI ET AL.	
	Terminal Disclaimer Filed: 03/29/2006		

TERMINAL DISCLAIMER	<input checked="" type="checkbox"/> APPROVED	<input type="checkbox"/> DISAPPROVED
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NOTE DISCLAIMER:

The term of this patent shall not extend
beyond the expiration date of Pat. No. 6,331,457.

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Docket No. 0756-2659

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of
SHUNPEI YAMAZAKI et al.

Serial No. 10/678,139

Filed: October 6, 2003

For: METHOD FOR FABRICATING SEMICONDUCTOR THIN FILM)

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) Art Unit: 2822

) Examiner: GUERRERO

TERMINAL DISCLAIMER

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Sir:

I, Dr. Shunpei Yamazaki, having a place of business at Semiconductor Energy Laboratory Co., Ltd., 398, Hase, Atsugi-shi, Kanagawa-ken 243-0036 Japan, state that I am authorized to sign on behalf of the assignee of this invention and that the Assignment referred to below has been reviewed and certify that, to the best of my knowledge and belief, the entire right, title and interest in the above-identified application is in the name of Semiconductor Energy Laboratory Co., Ltd. by virtue of an Assignment recorded in the U.S. Patent and Trademark Office at Reel 07701, Frame 0233-0225.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

09/20/2006

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